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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of:  
Park, et al.

Serial No.: 10/604,912

Filed: August 26, 2003

Group Art Unit: 2823

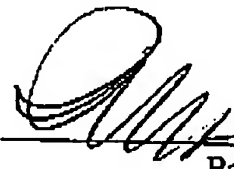
Examiner: Kebede, Brook

Atty. Docket No.: FIS920030026US1

For: METHOD TO PRODUCE TRANSISTOR HAVING REDUCED GATE  
HEIGHT

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Pamela M. Riley

SUBMISSION IN SUPPORT OF REQUEST FOR CONTINUED EXAMINATION  
UNDER 37 C.F.R. §1.114

Mail Stop Non-Fee Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

This submission is in support of the request for continued examination filed  
herewith under 37 C.F. R. §1.116 in response to the Office Action mailed October 4,  
2005, setting a three-month statutory period for response. Therefore, this submission is  
timely filed. Please amend the above-identified patent application as follows:

10/604,912

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